

# 1N4001 THRU 1N4007

## Surface Mount General Purpose Silicon Rectifiers

Reverse Voltage - 50 to 1000 V

Forward Current - 1 A

### FEATURES

- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Ideal for automated placement
- Lead free in comply with EU RoHS 2011/65/EU directives

### MECHANICAL DATA

- Case: SOD-123FL
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 15mg / 0.00053oz

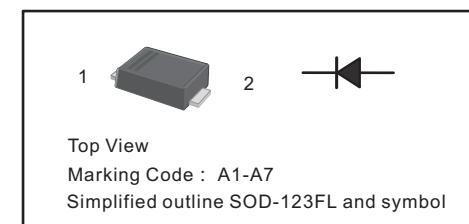
### Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

#### PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode

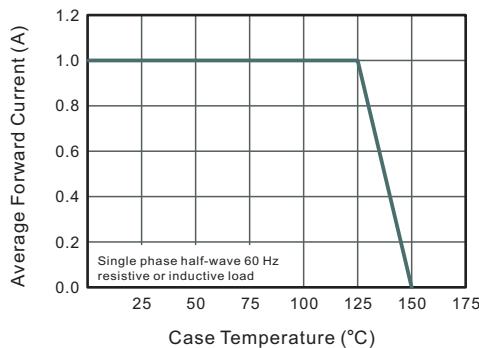


Parameter	Symbols	1N4001	1N4002	1N4003	1N4004	1N4005	1N4006	1N4007	Units
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at T <sub>c</sub> = 125 °C	I <sub>F(AV)</sub>	1							A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	30							A
Maximum Instantaneous Forward Voltage at 1 A	V <sub>F</sub>	1.1							V
Maximum DC Reverse Current T <sub>a</sub> = 25 °C at Rated DC Blocking Voltage T <sub>a</sub> = 125 °C	I <sub>R</sub>	5 50							μA
Typical Junction Capacitance <sup>(1)</sup>	C <sub>j</sub>	8(TYP.)							pF
Typical Thermal Resistance <sup>(2)</sup>	R <sub>θJA</sub>	90							°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55 ~ +150							°C

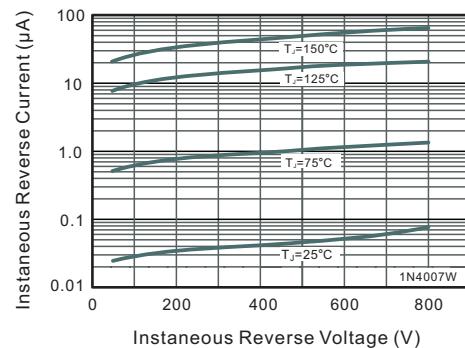
( 1 ) Measured at 1 MHz and applied reverse voltage of 4 V D.C

( 2 ) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.

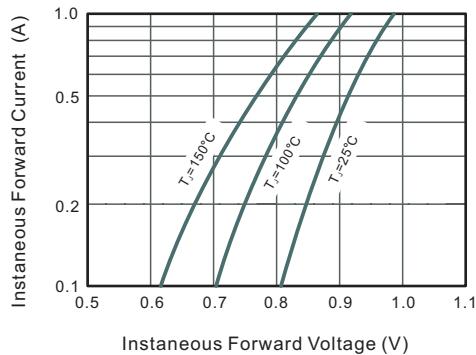
**Fig.1 Forward Current Derating Curve**



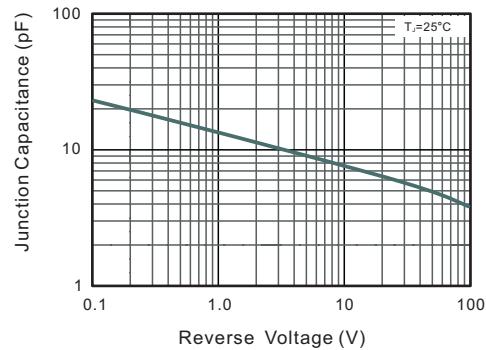
**Fig.2 Typical Instantaneous Reverse Characteristics**



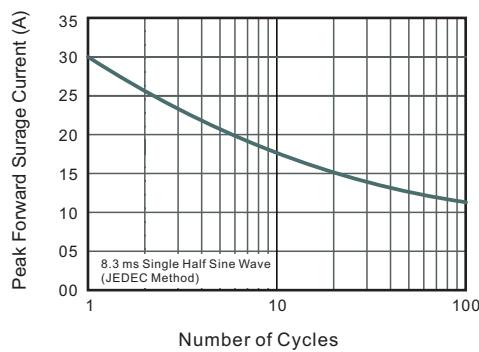
**Fig.3 Typical Forward Characteristic**



**Fig.4 Typical Junction Capacitance**

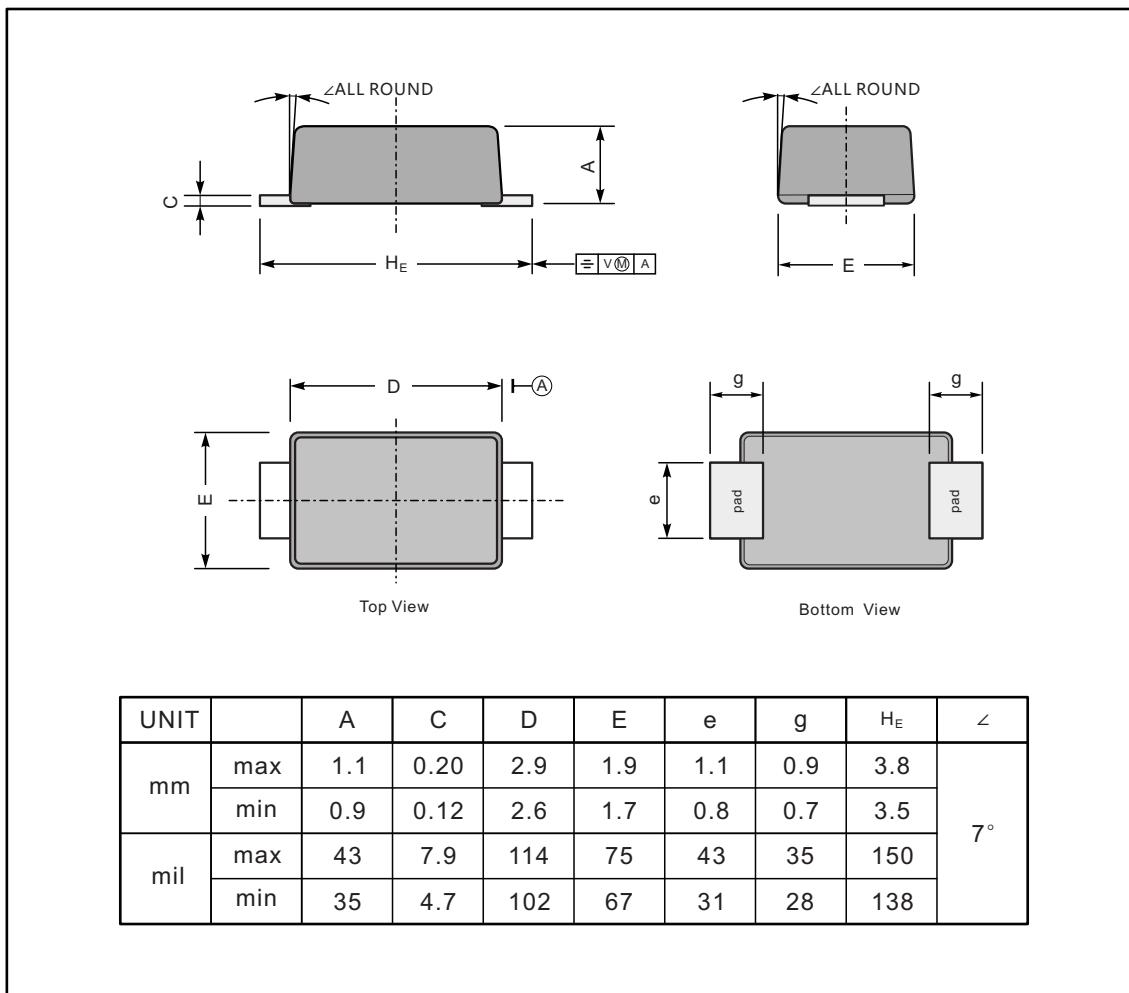


**Fig.5 Maximum Non-Repetitive Peak Forward Surge Current**



## PACKAGE OUTLINE

Plastic surface mounted package; 2 leads



### The recommended mounting pad size

